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IR-2132 (2-3575)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Thomas J. Ribarich

Serial No.: 10/615,710

Filed: July 8, 2003

New York, New York

Date: October 29, 2004

Group Art Unit: 2821

Examiner: Ephrem Alemu

For: CHIP BALLAST CONTROL WITH POWER FACTOR CORRECTION

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

SUBMISSION

Sir:

Submitted herewith is a copy of a search report issued by a patent searching authority other than the PTO and the cited art together with a form listing the same for the convenience of the Examiner.

I certify that each item of information contained in this Submission was cited in a communication from a foreign patent office in a counterpart foreign application and that this communication was not received by any individual designated in 37 C.F.R. 1.56(c) more than 30 days prior to the filing of this Submission.

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Respectfully submitted,

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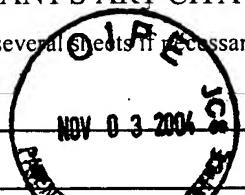
Name of applicant, assignee or
Registered Representative

Signature
October 29, 2004

Date of Signature

BJK:gl
Enclosures

APPLICANT'S ART CITATION (Use several sheets if necessary)	Application 10/615,710	OFGS File No. IR-2132 (2-3575)
	Applicant Thomas Ribarich	
	Filing Date July 8, 2003	Group Art Unit 2821



U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date MM-YYYY	Name	Class	Sub-class	Filing Date If Appropriate
	US-6,211,623	04-2001	WILHELM ET AL.			
	US-					
	US-					
	US-					
	US-					
	US-					
	US-					
	US-					
	US-					
	US-					

FOREIGN PATENT DOCUMENTS

	Document Number	Date MM-YYYY	Country	Class	Sub-class	Translation	
						Yes	No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner _____ Date Considered _____

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.